(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 20 November 2003 (20.11.2003)

PCT

(10) International Publication Number WO 03/096114 A1

(51) International Patent Classification7:

(21) International Application Number:

PCT/KR02/01392

G02F 1/136

(22) International Filing Date:

24 July 2002 (24.07.2002)

(25) Filing Language:

Korean

(26) Publication Language:

English

(30) Priority Data:

2002-25536

9 May 2002 (09.05.2002)

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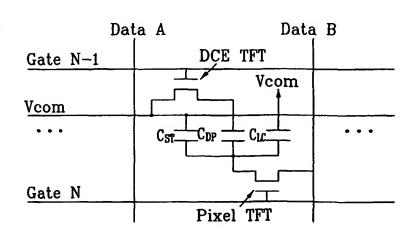
- CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: MULTI-DOMAIN LIQUID CRYSTAL DISPLAY AND A THIN FILM TRANSISTOR SUBSTRATE OF THE SAME



(57) Abstract: A thin film transistor array panel is provided, which includes: a gate wire formed on an insulating substrate; a data wire formed on the insulating substrate, insulated from the gate wire, and intersecting the gate wire; a storage electrode wire formed on the insulating substrate, insulated from the data wire, and intersecting the data wire; a plurality of pixel electrodes provided on the respective pixel areas defined by the intersections of the gate wire and the data wire, each pixel electrode having a cutout; a plurality of direction control electrodes provided on the respective pixel areas defined by the intersections of the gate wire and the data wire; a plurality of first thin film transistors connected to the gate wire, the data wire,

and the pixel electrodes; and a plurality of second thin film transistors connected to the gate wire, the storage electrode wire, and the direction control electrodes.

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nternational application No.

PCT/KR02/01392

A. CLAS	SIFICATION OF SUBJECT MATTER				
PC7 G02F 1/136					
According to International Patent Classification (IPC) or to both national classification and IPC					
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols)					
Minimum documentation searched (classification system followed by classification symbols)					
IPC7 C	G02F, H01L				
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Electronic data	a base consulted during the intertnational search (name	of data base and, where practicable, search terr	ns used)		
PAJ " semiconductor" "line" "gate" " x type" " 1st line" "2nd line" "3rd line" "amorphous silicon" "insulator"					
C. DOCUN	MENTS CONSIDERED TO BE RELEVANT				
			Relevant to claim No.		
Category*	Citation of document, with indication, where app	ropriate, of the relevant passages	Relevant to claim No.		
A	JP 55-7126 (HITACHI LTD) 30, July, 1980		1-16		
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Further documents are listed in the continuation of Box C. X See patent family annex.					
* Special c	Special categories of cited documents: "T" later document published after the international filing date of priority				
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"P" documen	t published prior to the international filing date but later		-		
Date of the ac	priority date claimed tual completion of the international search	Date of mailing of the international search re	port		
2	9 NOVEMBER 2002 (29.11.2002)	29 NOVEMBER 2002 (29.11.20	002)		
Name and ma	ailing address of the ISA/KR	Authorized officer			
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	920 Dunsan-dong, Seo-gu, Daejeon 302-701, Republic of Korea	Cito, Kyoung riwa	([[]]]		
Esserimite Me	92 42 472 7140	Telephone No. 82-42-481-5767	L'Asim "		



Information on patent family members

international application No.

PCT/KR02/01392

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
JP 55-7126	30 - 07 - 1980	None	
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